## Clock Page Replacement

2024 Winter ECE 353: Systems Software Jon Eyolfson

## Clock Algorithm

Data structures:

- Keeps a circular list of pages in memory
- Uses a reference bit for each page in memory (light grey in next slides)
- Has a "hand" (iterator) pointing to the last element examined

Algorithm, to insert a new page:

- Check the hand's reference bit, if it's 0 then place the page and advance hand
- If the reference bit is 1 , set it to 0 , advance the hand, and repeat

For page accesses, set the reference bit to 1

## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)


## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)

1


## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)

12


## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)

123


## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)

134


## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)
$\begin{array}{lllll}1 & 2 & 3 & 4\end{array}$


## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)
$\begin{array}{lllll}1 & 2 & 3 & 4\end{array}$


## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)
$\begin{array}{lllll}1 & 2 & 3 & 4 & 5\end{array}$


## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

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$\begin{array}{lllll}1 & 2 & 3 & 4\end{array}$


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## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

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| 1 | 2 | 3 | 4 | 5 | 2 | 3 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |



## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

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| 1 | 2 | 3 | 4 | 5 | 2 | 3 | 1 |
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## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)

| 1 | 2 | 3 | 4 | 5 | 2 | 3 | 1 | 2 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |



## Clock Example (with Diagram)

Assume our physical memory can only hold 4 pages, and we access the following:

1234523123 (all of the pages are initially on disk)

| 1 | 2 | 3 | 4 | 5 | 2 | 3 | 1 | 2 | 3 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |



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Assume our physical memory can only hold 4 pages, and we access the following:

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6 page faults

## For Performance You May Choose to Disable Swapping

Memory is cheap, and has quite high capacity
You'd rather know you need more memory than run slowly
Linux runs an OOM (out of memory) killer, that SIGKILLs the memory hog
Larger page sizes allow for speedups ( 2 MiB or 1 GiB page size)
Trade more fragmentation for more TLB coverage

## The Clock Algorithm is an Approximation of LRU

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